INTERSECTION:

Group Assignment:
Field Master Assignment:
System Reference Number:

N/S Street Name: **Doty**
E/W Street Name: **Century**

Last Database Change:

Change Record					
Change	By	Date	Change	By	Date

Notes: Temporary traffic signal timing during construction
As of 8/12/2016

Manual Plan
0 = Automatic
1-9 = Plan 1-9
14 = Free
15 = Flash

Manual Offset
0 = Automatic
1 = Offset A
2 = Offset B
3 = Offset C

Drop Number	9	<C/0+0+0>
Zone Number	0	<C/0+0+1>
Area Number	1	<C/0+0+2>
Area Address	86	<C/0+0+3>
QuicNet Channel	OP:8002:10.10.2	(QuicNet)

Communication Addresses

Manual Plan	14	<C/0+A+1>
Manual Offset	0	<C/0+B+1>

Manual Selection

Flash Start	2	<F/1+0+E>
Red Revert	2.0	<F/1+0+F>
All Red Start	5.0	<F/1+C+0>

Start / Revert Times

Exclusive Walk		<F/1+0+0>
Exclusive FDW		<F/1+0+1>
All Red Clear		<F/1+0+2>

Exclusive Ped Phase
(Outputs specified in Assignable Outputs at E/127+A+E & F)

Row	Column Numbers ---->	Phase 1	2	3	4	5	6	7	8
	Phase Names ---->								
0	Ped Walk	0	9	0	9	0	9	0	0
1	Ped FDW	0	20	0	30	0	24	0	0
2	Min Green	9	38	9	30	9	39	0	30
3	Type 3 Disconnect	0	0	0	0	0	0	0	0
4	Added per Vehicle	0.0	1.2	0.0	0.0	0.0	1.2	0.0	1.2
5	Veh Extension	0.5	3.5	0.5	3.0	2.0	3.5	0.0	3.0
6	Max Gap	0.5	5.0	0.5	3.0	2.0	5.0	0.0	3.0
7	Min Gap	0.5	2.0	0.5	3.0	2.0	2.0	0.0	3.0
8	Max Limit	17	57	17	30	28	48	0	30
9	Max Limit 2	30	70	30	70	30	70	0	70
A	Adv. / Delay Walk	0	0	0	0	0	0	0	0
B	PE Min Ped FDW	0	0	0	0	0	0	0	0
C	Cond Serv Check	1	1	1	1	1	1	0	1
D	Reduce Every	1.0	1.0	1.0	1.0	1.0	1.0	0.0	1.0
E	Yellow Change	4.4	4.4	3.6	3.6	4.4	4.4	0.0	3.6
F	Red Clear	0.5	1.0	0.5	1.0	0.5	1.0	0.0	1.0

Phase Timing - Bank 1 <C+0+F=1>

	9	A	B	C	D
	---	---	---	---	---
Phase 1	0				
Phase 2	20				
Phase 3	0				
Phase 4	20				
Phase 5	0				
Phase 6	20				
Phase 7	0				
Phase 8	20				
Max Initial					
Alternate Walk					
Alternate FDW					
Alternate Initial					
Alternate Extension					

Alternate Timing <C+0+F=1>

	E
RR-1 Delay	
RR-1 Clear	
EV-A Delay	
EV-A Clear	
EV-B Delay	
EV-B Clear	
EV-C Delay	
EV-C Clear	
EV-D Delay	
EV-D Clear	
RR-2 Delay	
RR-2 Clear	
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing

Row		F
0	Permit	1 2 3 4 5 6 7 8
1	Red Lock	
2	Yellow Lock	
3	Min Recall	1 2 3 4 5 6 7 8
4	Ped Recall	2 4 6
5	*View Set Peds*	2 4 6
6	Rest In Walk	
7	Red Rest	
8	Dual Entry	
9	Max Recall	
A	Soft Recall	
B	Max 2	
C	Cond. Service	
D	Man Cntrl Calls	
E	Yellow Start	4
F	First Phases	2 6

Phase Functions <C+0+F=1>

Row	Overlap	1	2	3	4	5	6	7	8
	Column Numbers ---->								
	Overlap Name ---->								
0	Load Switch Number								
1	Veh Set 1 - Phases								
2	Veh Set 2 - Phases								
3	Veh Set 3 - Phases								
4	Neg Veh Phases								
5	Neg Ped Phases								
6	Green Omit Phases								
7	Green Clear Omit Phs.								
8									
9									
A									
B									
C									
D	Green Clear								
E	Yellow Change								
F	Red Clear								

Overlap Assignments <C+0+E=29>

Extra 1 Flags
1 = TBC Type 1
2 = NEMA Ext. Coord
3 = Auto Daylight Savings
4 = Solid FDW on EV
5 = Extended Status
6 = International Ped
7 = Flash - Clear Outputs
8 = Split Ring

Extra 2 Flags
1 = AWB During Initial
2 = LMU Installed
3 = Disable Min Walk
4 = QuicNet/4 System
5 = Ignore P/P on EV
6 =
7 = Reserved
8 =

Row		C
0	EV-A	
1	EV-B	
2	EV-C	
3	EV-D	
4	RR-1 *	- - -
5	RR-2 *	- - -
6	SE-1	
7	SE-2	

Preempt Priority
<C+0+E=125>
(* RR-1 is always Highest, and RR-2 is always Second Highest)

Row	Column Numbers ---->	E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	
3	RR-2 Limited Service	
4	Prot / Perm Phases	3
5	Flash to PE Circuits	
6	Flash Entry Phases	
7	Disable Yellow Range	
8	Disable Ovp Yel Range	
9	Overlap Yellow Flash	
A	EV-A Phases	
B	EV-B Phases	
C	EV-C Phases	
D	EV-D Phases	
E	Extra 1 Config. Bits	1 3
F	IC Select (Interconnect)	2

Configuration <C+0+E=125>

	F
Ext. Permit 1 Phases	
Ext. Permit 2 Phases	
Exclusive Ped Assign	
Preempt Non-Lock	
Ped for 2P Output	2
Ped for 6P Output	6
Ped for 4P Output	4
Ped for 8P Output	
Yellow Flash Phases	
Low Priority A Phases	
Low Priority B Phases	
Low Priority C Phases	
Low Priority D Phases	
Restricted Phases	
Extra 2 Config. Bits	

Configuration <C+0+E=125>

	F
Fast Green Flash Phase	
Green Flash Phases	
Flashing Walk Phases	
Guaranteed Passage	
Simultaneous Gap Term	
Sequential Timing	
Advance Walk Phases	
Delay Walk Phases	
External Recall	
Start-up Overlap Green	
Max Extension	
Inhibit Ped Reservice	
Semi-Actuated	
Start-up Overlap Yellow	
Start-up Vehicle Calls	123456 8
Start-up Ped Calls	2 4

Specials <C+0+F=2>

Flash to PE & PE Non-Lock
1 = EV A 5 = RR 1
2 = EV B 6 = RR 2
3 = EV C 7 = SE 1
4 = EV D 8 = SE 2

IC Select Flags
1 =
2 = Modem
3 = 7-Wire Slave
4 = Flash / Free
5 =
6 = Simplex Master
7 = 7-Wire Master
8 = Offset Interrupter

Row		2
0		
1	Phase 1	10
2	Phase 2	10
3	Phase 3	10
4	Phase 4	10
5	Phase 5	10
6	Phase 6	10
7	Phase 7	10
8	Phase 8	10

Coordination Transition Minimums
<C+0+C=5>

Coord Extra
1 = Programmed WALK Time for Sync Phases
2 = Always Terminate Sync Phase Peds

Row	Column Numbers ---->	Plan 1	2	3	4	5	6	7	8	9
	Plan Name ---->									
0	Cycle Length	100	100	100	100	100	100	100	100	100
1	Phase 1 - ForceOff	55	55	55	55	55	55	55	55	55
2	Phase 2 - ForceOff									
3	Phase 3 - ForceOff	20	20	20	20	20	20	20	20	20
4	Phase 4 - ForceOff	40	40	40	40	40	40	40	40	40
5	Phase 5 - ForceOff	55	55	55	55	55	55	55	55	55
6	Phase 6 - ForceOff									
7	Phase 7 - ForceOff	20	20	20	20	20	20	20	20	20
8	Phase 8 - ForceOff	40	40	40	40	40	40	40	40	40
9	Ring Offset									
A	Offset 1									
B	Offset 2									
C	Offset 3									
D	Perm 1 - End	15	15	15	15	15	15	15	15	15
E	Hold Release	255	255	255	255	255	255	255	255	255
F	Zone Offset									

Coordination - Bank 1 <C+0+C=1>

Row		E
0		
1	Plan 1 - Sync	2 6
2	Plan 2 - Sync	2 6
3	Plan 3 - Sync	2 6
4	Plan 4 - Sync	2 6
5	Plan 5 - Sync	2 6
6	Plan 6 - Sync	2 6
7	Plan 7 - Sync	2 6
8	Plan 8 - Sync	2 6
9	Plan 9 - Sync	2 6
A	NEMA Sync	
B	NEMA Hold	
C		
D		
E	Coord Extra	
F		

Sync Phases <C+0+C=1>

Row		1	2	3	4	5	6	7	8	9
0	Ped Adjustment									
1	Perm 2 - Start									
2	Perm 2 - End									
3	Perm 3 - Start									
4	Perm 3 - End									
5	Reservice Time									
6	Reservice Phases									
7										
8	Pretimed Phases									
9	Max Recall									
A	Perm 1 Veh Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
B	Perm 1 Ped Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678
C	Perm 2 Veh Phase									
D	Perm 2 Ped Phase									
E	Perm 3 Veh Phase									
F	Perm 3 Ped Phase									

Coordination - Bank 2 <C+0+C=2>

Row		F
0	Free Lag	2 4 6 8
1	Plan 1 - Lag	2 4 6 8
2	Plan 2 - Lag	2 4 6 8
3	Plan 3 - Lag	2 4 6 8
4	Plan 4 - Lag	2 4 5 8
5	Plan 5 - Lag	2 4 6 8
6	Plan 6 - Lag	2 4 5 8
7	Plan 7 - Lag	2 4 6 8
8	Plan 8 - Lag	2 4 6 8
9	Plan 9 - Lag	2 4 6 8
A	External Lag	
B		
C		
D		
E		
F		

Lag Phases <C+0+C=1>

Row	Column 9		Column A		Column B		Column C		Column D		Column E		Column F		Row
0	Spec. Funct. 1		NOT-3		Max 2		Pretimed		Set Monday		Dial 2 (7-Wire)		Sim Term		0
1	Spec. Funct. 2		NOT-4		System Det 1		Plan 1		Ext. Perm 1		Dial 3 (7-Wire)		EV-A	71	1
2	Spec. Funct. 3		OR-4 (a)		System Det 2		Plan 2		Ext. Perm 2		Offset 1 (7-Wire)		EV-B	72	2
3	Spec. Funct. 4		OR-4 (b)		System Det 3		Plan 3		Dimming		Offset 2 (7-Wire)		EV-C	73	3
4	NAND-3 (a)		OR-5 (a)		System Det 4		Plan 4		Set Clock		Offset 3 (7-Wire)		EV-D	74	4
5	NAND-3 (b)		OR-5 (b)		System Det 5		Plan 5		Stop Time	82	Free (7-Wire)		RR-1	51	5
6	NAND-4 (a)		OR-6 (a)		System Det 6		Plan 6		Flash Sense	81	Flash (7-Wire)		RR-2	52	6
7	NAND-4 (b)		OR-6 (b)		System Det 7		Plan 7		Manual Enable		Excl. Ped Omit		Spec. Event 1		7
8	OR-7 (a)		Fig 3 Diamond		System Det 8		Plan 8		Man. Advance		NOT-1		Spec. Event 2		8
9	OR-7 (b)		Fig 4 Diamond		Max Inhibit (nema)		Plan 9		External Alarm		NOT-2		External Lag		9
A	OR-7 (c)		AND-4 (a)		Force A (nema)		DELAY-A		Phase Bank 2		OR-1 (a)		AND-1 (a)		A
B	OR-7 (d)		AND-4 (b)		Force B (nema)		DELAY-B		Phase Bank 3		OR-1 (b)		AND-1 (b)		B
C	OR-8 (a)		NAND-1 (a)		C.N.A. (nema)		DELAY-C		Overlap Set 2		OR-2 (a)		AND-2 (a)		C
D	OR-8 (b)		NAND-1 (b)		Hold (nema)		DELAY-D		Overlap Set 3		OR-2 (b)		AND-2 (b)		D
E	OR-8 (c)		NAND-2 (a)		Max Recall		DELAY-E		Detector Set 2		OR-3 (a)		AND-3 (a)		E
F	OR-8 (d)		NAND-2 (b)		Min Recall		DELAY-F		Detector Set 3		OR-3 (b)		AND-3 (b)		F

Assignable Inputs <C+0+E=126>

Row	Column 9		Column A		Column B		Column C		Column D		Column E		Column F		Row
0	Phase ON - 1		Preempt Fail		Flasher 0		Free		NOT-1		TOD Out 1		Dial 2 (7-Wire)		0
1	Phase ON - 2		Sp Evnt Out 1		Flasher 1		Plan 1		OR-1		TOD Out 2		Dial 3 (7-Wire)		1
2	Phase ON - 3		Sp Evnt Out 2		Fast Flasher		Plan 2		OR-2		TOD Out 3		Offset 1 (7-Wire)		2
3	Phase ON - 4		Sp Evnt Out 3		Fig 3 Diamond		Plan 3		OR-3		TOD Out 4		Offset 2 (7-Wire)		3
4	Phase ON - 5		Sp Evnt Out 4		Fig 4 Diamond		Plan 4		AND-1		TOD Out 5		Offset 3 (7-Wire)		4
5	Phase ON - 6		Sp Evnt Out 5				Plan 5		AND-2		TOD Out 6		Free (7-Wire)		5
6	Phase ON - 7		Sp Evnt Out 6				Plan 6		AND-3		TOD Out 7		Flash (7-Wire)		6
7	Phase ON - 8		Sp Evnt Out 7				Plan 7		NOT-2		TOD Out 8		Preempt		7
8	Ph. Check - 1		Sp Evnt Out 8		NOT-3		Plan 8		EV-A		Adv. Warn - 1		Low Priority A		8
9	Ph. Check - 2				NOT-4		Plan 9		EV-B		Adv. Warn - 2		Low Priority B		9
A	Ph. Check - 3		Detector Fail		OR-4		Spec. Funct. 3		EV-C		DELAY-A		Low Priority C		A
B	Ph. Check - 4		Spec. Funct. 1		OR-5		Spec. Funct. 4		EV-D		DELAY-B		Low Priority D		B
C	Ph. Check - 5		Spec. Funct. 2		OR-6		NAND-3		RR-1		DELAY-C				C
D	Ph. Check - 6		Central Control		AND-4		NAND-4		RR-2		DELAY-D				D
E	Ph. Check - 7		Excl. Ped DW		NAND-1		OR-7		Spec. Event 1		DELAY-E				E
F	Ph. Check - 8		Excl. Ped WK		NAND-2		OR-8		Spec. Event 2		DELAY-F				F

Assignable Outputs <C+0+E=127>

Row	Column Numbers ---->	Phase 1	2	3	4	5	6	7	8
	Phase Names ---->								
0	Ped Walk								
1	Ped FDW								
2	Min Green								
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension								
6	Max Gap								
7	Min Gap								
8	Max Limit								
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW								
C	Cond Serv Check								
D	Reduce Every								
E	Yellow Change								
F	Red Clear								

Phase Timing - Bank 2 <C+0+F=2>

	9	A	B	C	D
	---	---	---	---	---
Phase 1					
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					
Max Initial					
Alternate Walk					
Alternate FDW					
Alternate Initial					
Alternate Extension					

Alternate Timing

Row		1	2	3	4	5	6	7	8
0	Ped Walk								
1	Ped FDW								
2	Min Green								
3	Type 3 Disconnect								
4	Added per Vehicle								
5	Veh Extension								
6	Max Gap								
7	Min Gap								
8	Max Limit								
9	Max Limit 2								
A	Adv. / Delay Walk								
B	PE Min Ped FDW								
C	Cond Serv Check								
D	Reduce Every								
E	Yellow Change								
F	Red Clear								

Phase Timing - Bank 3 <C+0+F=3>

	9	A	B	C	D
	---	---	---	---	---
Phase 1					
Phase 2					
Phase 3					
Phase 4					
Phase 5					
Phase 6					
Phase 7					
Phase 8					
Max Initial					
Alternate Walk					
Alternate FDW					
Alternate Initial					
Alternate Extension					

Alternate Timing

Transition Type
0.X = Shortway
1.X = Lengthen
X.1 thru X.4 = Number of cycles when lengthing

Daylight Savings Date
If set to all zeros, standard dates will be used.

Low Priority
1 = Channel A
2 = Channel B
3 = Channel C
4 = Channel D

Transition Type	0.3	<C/5+1+9>

TBC Transition

Lag Hold Phases		<C/5+1+A>

Coordinated Lag Hold Phases

Sync Output Time		<C/5+1+C>

7-Wire Master

Begin Month	3	<C/5+2+A>
Begin Week	2	<C/5+2+B>
End Month	11	<C/5+2+C>
End Week	1	<C/5+2+D>

Daylight Savings Time

Time B4 Yellow		<F/1+C+E>
Phase Number		<F/1+C+F>

Advance Warning Beacon - Sign 1

Time B4 Yellow		<F/1+D+E>
Phase Number		<F/1+D+F>

Advance Warning Beacon - Sign 2

Long Failure	0.7	<F/1+0+6>
Short Failure	0.7	<F/1+0+7>

Power Cycle Correction (Default = 0.7)

Min Time (seconds)		<F/1+0+8>

Min Green Before PE Force Off

Max Time (minutes)	255	<F/1+0+9>

Max Preempt Time Before Failure

Min Time (seconds)		<F/1+0+A>

Min Time Between Same Preempts
(Does Not Apply To Railroad Preempt)

Low Pri. Channel		<E/125+C+8>

Disable Low Priority Channel

Row	Detector Name	0 C1 Pin Number	1 Attributes	2 Phase(s)	3 Assign	1/10 Delay	3 Carry-over
0		39	45 7	2	123 8		
1		40	45 7	6	123 8		
2		41	45 7	4	123 8		
3		42	45 7	8	123 8		
4		43	45 7	2	123 8		
5		44	45 7	6	123 8		
6		45	45 7	4	123 8		
7		46	45 7	8	123 8		
8		47	67	2	123 8		
9		48	67	6	123 8		
A		49	67	4	123 8		
B		50	67	8	123 8		
C		55	45 7	5	123 8		
D		56	45 7	1	123 8		
E		57	45 7	7	123 8		
F		58	45 7	3	123 8		

Row	Detector Name	4 C1 Pin Number	5 Attributes	6 Phase(s)	7 Assign	2/10 Delay	4 Carry-over
0		59	45 7	5	123 8		
1		60	45 7	1	123 8		
2		61	45 7	7	123 8		
3		62	45 7	3	123 8		
4		63	45 7	2	123 8		
5		64	45 7	6	123 8		
6		65	45 7	4	123 8		
7		66	45 7	8	123 8		
8		67	2	2	123 8		
9		68	2	6	123 8		
A		69	2	4	123 8		
B		70	2	8	123 8		
C		76	45 7	2	123 8		
D		77	45 7	6	123 8		
E		78	45 7	4	123 8		
F		79	45 7	8	123 8		

Detector Assignments <C+0+E=126> <C+0+D=0>

Detector Attributes
1 = Full Time Delay
2 = Ped Call
3 =
4 = Count
5 = Extension
6 = Type 3
7 = Calling
8 = Alternate

Det. Assignments
1 = Det. Set 1
2 = Det. Set 2
3 = Det. Set 3
4 =
5 =
6 = Failure - Min Recall
7 = Failure - Max Recall
8 = Report on Failure

Column Numbers --->	Ped / Phase / Overlap 1	2	3	4	5	6	7	8	Row
Walk									0
Don't Walk									1
Phase Green									2
Phase Yellow									3
Phase Red									4
Overlap Green									5
Overlap Yellow									6
Overlap Red									7

Redirect Phase Outputs <C+0+E=127>

Cabinet Type		<E/125+D+0>

Enable Redirection
(Enable Redirection = 30)

Max OFF (minutes)		<D/0+0+1>
Max ON (minutes)		<D/0+0+2>

Detector Failure Monitor

	D	Row
		0
Output Port 1		1
Output Port 2		2
Output Port 3		3
Output Port 4		4
Output Port 5		5
Output Port 6		6
Output Port 7		7

Dimming <C+0+E=125>

	D
Number of Digits	
1 st Digit	
2 ed Digit	
3 ed Digit	
4 th Digit	
5 th Digit	
6 th Digit	
7 th Digit	
8 th Digit	
9 th Digit	
10 th Digit	
11 th Digit	
12 th Digit	
13 th Digit	
14 th Digit	
15 th Digit	

<C+0+C=5>

Dial-Back Telephone Number

Disable Alarms
1 = Stop Time
2 = Flash Sense
3 = Keyboard Entry
4 = Manual Plan
5 = Police Control
6 = External Alarm
7 = Detector Failure
8 =

	1/8	Row
DELAY-A		A
DELAY-B		B
DELAY-C		C
DELAY-D		D
DELAY-E		E
DELAY-F		F

Delay Logic Times
<C+0+D=0> (seconds)

Omit Alarm		<C/5+F+0>

Disable Alarm Reporting

Time	10	<C/5+C+0>

Redial Time (minutes)
(View Redial Timer at E/2+D+6)

Row	Time	Plan	Offset	Day of Week
0	:			
1	:			
2	:			
3	:			
4	:			
5	:			
6	:			
7	:			
8	:			
9	:			
A	:			
B	:			
C	:			
D	:			
E	:			
F	:			

TOD Coordination <C+0+9=0.1>
(Bank 1)

Time	Funct.	Day of Week
06 : 00	1	23456
09 : 30	1	1234567
14 : 30	1	23456
19 : 30	1	23456
22 : 00	1	1234567
:		
:		
:		
:		
:		
:		
:		
:		
:		
:		
:		
:		
:		
:		

TOD Function <C+0+7=0.1>

Column 4 Phases/Bits

<C+0+E=27>

Day	Year	Month	Holiday Type

Holiday Dates <C+0+8=1.1>
(Bank 1)

Time	Plan	Offset	Holiday Type
:			
:			
:			

Row	6 Clear	7 Time	8 Ped Call	9 Hold	A Advance	B Force Off	C Vehicle Call	D Permit Phases	E Ped Omit	F Output
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

Special Event Schedule -- Table 1 <C+0+E=27>

Notes:

[ ] <E/27+5+F>
Limited Service Interval

Row	6 Clear	7 Time	8 Ped Call	9 Hold	A Advance	B Force Off	C Vehicle Call	D Permit Phases	E Ped Omit	F Output
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
A										
B										
C										
D										
E										
F										

Special Event Schedule -- Table 2 <C+0+E=28>

Notes:

[ ] <E/28+5+F>
Limited Service Interval